04/30/01 A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Joseph P. Scannell

Serial No.: Not Assigned (converting from U.S. Provisional No. 60/200,114)

Filed: April 27, 2001

For: Bridge Scour Monitoring System and Process

Attorney Docket No.: USB-001.01

Group Art Unit: Not Assigned

Examiner: Not Assigned

Commissioner for Patents
Washington, D.C. 20231
Box: Patent Application

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Date of Deposit April 27, 2001

I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner for Patents, Box Patent Application, Washington, DC 20231.

Signature

William D. DeVaul

Please Print Name of Person Signing

REQUEST FOR CONVERSION FROM PROVISIONAL TO NON-PROVISIONAL AND PRELIMINARY AMENDMENT

Dear Sir:

Applicant hereby requests conversion of the above captioned application from a provisional application to a non-provisional application in accordance with 37 C.F.R. § 1.53(c)(3).[1] Applicant also files with this communication a preliminary amendment adding a Cross Reference to Related Applications and one new claim. Applicant also files herewith a Declaration and Power of Attorney and a check in the amount of $65.00

[1] Applicant notes that prior to May 29, 2000, conversion was possible using the procedure in 37 C.F.R. § 1.182. Should it be determined that C.F.R. § 1.53(c)(3) is inapplicable, Applicant requests consideration under C.F.R. § 1.182.

ATTORNEY DOCKET NO.: AVX-38-RE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application Reissue of Cain et al.	)	Examiner: Unknown
Patent No.: 5,898,562	)	Art Unit: Unknown
Issued: April 27, 1999	)	Deposit Acct. No.: 04-1403
Title: Integrated Dual Frequency Noise Attenuator	)	

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

The present preliminary amendment relates to the above-identified reissue application of U.S. Patent No. 5,898,562. Please enter the following broadening preliminary amendment:

IN THE DRAWINGS

Please replace figure 2 with the attached corrective drawing marked amended Figure 2.

IN THE CLAIMS

Please add new claims 13-15 as follows:

13. An integrated ceramic dual frequency bypass device including a parallel connected pair of ceramic capacitors of disparate values comprising at least one planar

dieletric layer, a first electrode and a second electrode disposed on respective opposite surfaces of said dielectric layer, said first electrode and said second electrode each being of generally u-shape and including a base portion and a pair of leg portions extending from said base portion, said base portion of said first electrode being disposed at an opposite end of said dielectric layer from said base portion of said second electrode, each of said leg portions of said first electrode being disposed in registry with an area of respective said leg portions of said second electrode, the areas of registration being of different values whereby a capacitance defined between a first registering pair of said leg portions differs from a capacitance defined between a second registering pair of said leg portions.

14. An integrated ceramic bypass device comprising:

the device body having a first polarity termination and a second polarity termination located on respective opposite ends thereof;

said device body comprising a plurality of dielectric layers, each said dielectric layer having either a first pair of leg portions extending from said first termination or a second pair of leg portions extending from said second termination thereon;

wherein one of said first leg portions overlaps a corresponding one of said second leg portions to define a first predetermined capacitance value;

wherein another of said first leg portions overlaps a corresponding other of said leg portions to define a second predetermined capacitance value; and

wherein the first area of overlap defined by said one of said first pair of leg portions and said corresponding one of said second pair of leg portions is

substantially three times a second area of overlap defined by said another said first pair of leg portions and said corresponding other of said second pair of leg portions.

15. An integrated ceramic and bypass device providing a pair of parallel connected ceramic capacitors of disparate capacitor value in a single component package, said device comprising;

device body having a first polarity termination and a second polarity termination located on respective opposite ends thereof;

said device body comprising a plurality of dielectric layers, each dielectric layer having either a first pair of leg portions extending from said first termination or a second pair of leg portions extending from said second termination thereon;

one of said first leg portions overlapping a corresponding one of said second leg portions to define a first predetermined capacitance value;

and another of said first leg portions overlapping a corresponding other of said second leg portions to define a second predetermined capacitance value, whereby first and second and parallel capacitors of said disparate capacitance values are provided.

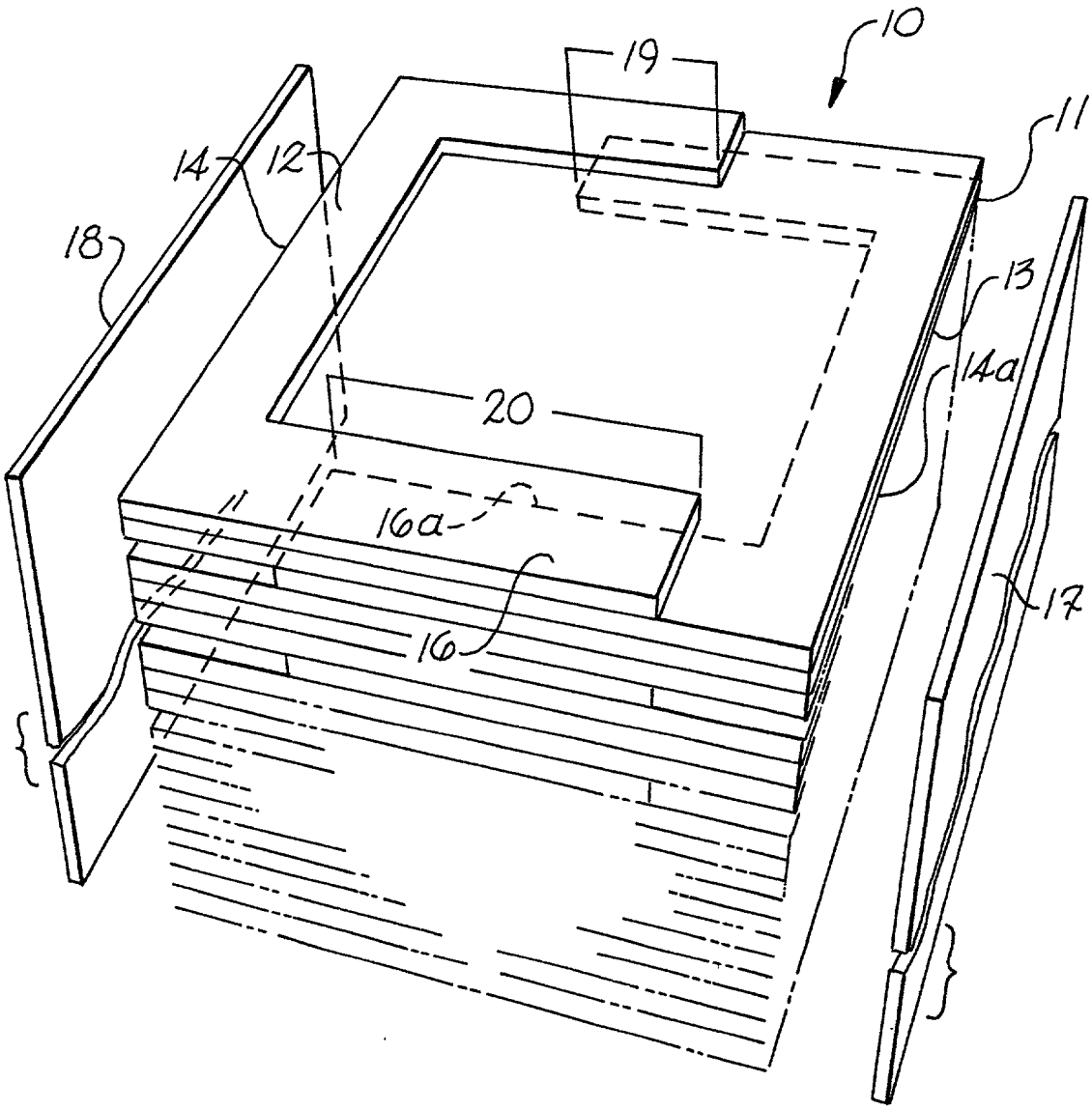

Fig. 2